

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

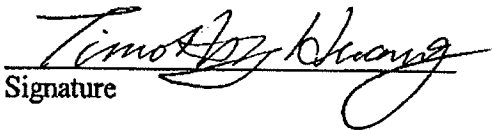
REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY
AND CHANGE OF CORRESPONDENCE ADDRESS

We hereby revoke all previous powers of attorney given in the US applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399 and appoint the practitioners associated with the Customer Number 25570 to act on our behalf for each of the identified US applications and/or patents recorded by U.S. Patent and Trademark Office on July 13, 2009 in Reel/Frame 022939/0399.

Please change the correspondence address for each of the applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399 for all purposes, including the fee address, to the address associated with Customer Number 25570.

Patent Owner, Microelectronic Technology, Inc., is the Assignee of record of the entire interest. A Statement under 37 C.F.R. 3.73(b) is enclosed. The undersigned is authorized to act on behalf of the assignee.

Signature



Date

7-16-2009

Mr. Timothy HUANG
Authorized representative of
MICROELECTRONICS TECHNOLOGY, INC.

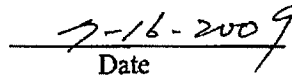
IN THE UNITED STATES PATENT & TRADEMARK OFFICE

STATEMENT UNDER 37 CFR 3.73(b)

Patent Owner, Microelectronic Technology, Inc., a corporation, states that it is the assignee of the entire right, title, and interest in the U.S. patent applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399.

The undersigned is authorized to act on behalf of the assignee.


Signature


Date

Mr. Timothy HUANG
Authorized representative of
MICROELECTRONICS TECHNOLOGY, INC.

Please address all the communication to the customer number 25570 associated with the below address:

Roberts Mlotkowski Safran & Cole P.C.
Intellectual Property Department
P.O. Box 10064
McLean, VA 22102
Telephone: 703-584-3270
Facsimile: 703-848-2981



United States Patent and Trademark Office

[Home](#) | [Site Index](#) | [Search](#) | [Guides](#) | [Contacts](#) | [eBusiness](#) | [eBiz alerts](#) | [News](#) | [Help](#)



Assignments on the Web > Patent Query

Patent Assignment Details

**NOTE: Results display only for issued patents and published applications.
For pending or abandoned applications please consult USPTO staff.**

Reel/Frame: 022939/0399

Pages: 15

Recorded: 07/13/2009

Attorney Dkt #: 14485.13

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Total properties: 79

1	Patent #: <u>4941153</u>	Issue Dt: 07/10/1990	Application #: 07089281	Filing Dt: 08/25/1987
	Title: HIGH-SPEED DIGITAL DATA COMMUNICATION SYSTEM			
2	Patent #: <u>4975931</u>	Issue Dt: 12/04/1990	Application #: 07286435	Filing Dt: 12/19/1988
	Title: HIGH SPEED PROGRAMMABLE DIVIDER			
3	Patent #: <u>5058107</u>	Issue Dt: 10/15/1991	Application #: 07293894	Filing Dt: 01/05/1989
	Title: EFFICIENT DIGITAL FREQUENCY DIVISION MULTIPLEXED SIGNAL RECEIVER			
4	Patent #: <u>5130578</u>	Issue Dt: 07/14/1992	Application #: 07444454	Filing Dt: 11/30/1989
	Title: EFFICIENT HIGH SPEED N-WORD COMPARATOR			
5	Patent #: <u>5128534</u>	Issue Dt: 07/07/1992	Application #: 07554238	Filing Dt: 07/17/1990
	Title: HIGH CHARGE CAPACITY FOCAL PLANE ARRAY READOUT CELL			
6	Patent #: <u>5271038</u>	Issue Dt: 12/14/1993	Application #: 07580710	Filing Dt: 09/10/1990
	Title: DISTORTION SUPPRESSION USING THRESHOLDING TECHNIQUES			
7	Patent #: <u>5303161</u>	Issue Dt: 04/12/1994	Application #: 07624958	Filing Dt: 12/10/1990
	Title: TECHNOLOGY INDEPENDENT INTEGRATED CIRCUIT MASK ARTWORK GENERATOR			
8	Patent #: <u>5164959</u>	Issue Dt: 11/17/1992	Application #: 07643969	Filing Dt: 01/22/1991
	Title: DIGITAL EQUALIZATION METHOD AND APPARATUS			
9	Patent #: <u>5267272</u>	Issue Dt: 11/30/1993	Application #: 07655684	Filing Dt: 02/14/1991
	Title: A RECEIVER AUTOMATIC GAIN CONTROL (AGC)			
10	Patent #: <u>5136205</u>	Issue Dt: 08/04/1992	Application #: 07675590	Filing Dt: 03/26/1991
	Title: MICROELECTRONIC FIELD EMISSION DEVICE WITH AIR BRIDGE ANODE			
11	Patent #: <u>5128674</u>	Issue Dt: 07/07/1992	Application #: 07676635	Filing Dt: 03/28/1991
	Title: TWO QUADRANTS HIGH SPEED MULTIPLYING DAC			
12	Patent #: <u>5206647</u>	Issue Dt: 04/27/1993	Application #: 07722763	Filing Dt: 06/27/1991
	Title: LOW COST AGC FUNCTION FOR MULTIPLE APPROXIMATION A/D CONVERTERS			

13	Patent #: <u>5251218</u> Title: EFFICIENT DIGITAL FREQUENCY DIVISION MULTIPLEXED SIGNAL RECEIVER	Issue Dt: 10/05/1993	Application #: 07739593	Filing Dt: 07/31/1991
14	Patent #: <u>5220557</u> Title: MULTIPLE USE DIGITAL TRANSMITTER/TRANSCIVER WITH TIME MULTIPLEXING	Issue Dt: 06/15/1993	Application #: 07765157	Filing Dt: 09/23/1991
15	Patent #: <u>5304951</u> Title: DIVIDER SYNCHRONIZATION CIRCUIT FOR PHASE-LOCKED LOOP FREQUENCY SYNTHESIZER	Issue Dt: 04/19/1994	Application #: 07829183	Filing Dt: 01/31/1992
16	Patent #: <u>5313113</u> Title: SAMPLE AND HOLD CIRCUIT WITH FULL SIGNAL MODULATION COMPENSATION USING BIPOLAR TRANSISTORS OF SINGLE CONDUCTIVITY TYPE	Issue Dt: 05/17/1994	Application #: 07870369	Filing Dt: 04/17/1992
17	Patent #: <u>5250911</u> Title: SINGLE-ENDED AND DIFFERENTIAL TRANSISTOR AMPLIFIER CIRCUITS WITH FULL SIGNAL MODULATION COMPENSATION TECHNIQUES WHICH ARE TECHNOLOGY INDEPENDENT	Issue Dt: 10/05/1993	Application #: 07871861	Filing Dt: 04/20/1992
18	Patent #: <u>5315169</u> Title: POWER-EFFICIENT SAMPLE AND HOLD CIRCUIT USING BIPOLAR TRANSISTORS OF SINGLE CONDUCTIVITY TYPE	Issue Dt: 05/24/1994	Application #: 07894980	Filing Dt: 06/08/1992
19	Patent #: <u>5278837</u> Title: MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH COMBINED MULTIPLE FREQUENCY CHANNELS	Issue Dt: 01/11/1994	Application #: 07905965	Filing Dt: 06/29/1992
20	Patent #: <u>5350952</u> Title: SAMPLE AND HOLD CIRCUIT WITH PUSH-PULL OUTPUT CHARGING CURRENT	Issue Dt: 09/27/1994	Application #: 07909286	Filing Dt: 07/06/1992
21	Patent #: <u>5315231</u> Title: SYMMETRICAL BIPOLAR BIAS CURRENT SOURCE WITH HIGH POWER SUPPLY REJECTION RATIO (PSRR)	Issue Dt: 05/24/1994	Application #: 07976760	Filing Dt: 11/16/1992
22	Patent #: <u>5621730</u> Title: MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH TIME DIVISION MULTIPLEXING	Issue Dt: 04/15/1997	Application #: 07986180	Filing Dt: 12/07/1992
23	Patent #: <u>5483150</u> Title: TRANSISTOR CURRENT SWITCH ARRAY FOR DIGITAL-TO-ANALOG CONVERTER (DAC) INCLUDING BIAS CURRENT COMPENSATION FOR INDIVIDUAL TRANSISTOR CURRENT GAIN AND THERMALLY INDUCED BASE-EMITTER VOLTAGE DROP VARIATION	Issue Dt: 01/09/1996	Application #: 08017200	Filing Dt: 02/05/1993
24	Patent #: <u>5343163</u> Title: SINGLE-ENDED AND DIFFERENTIAL TRANSISTOR AMPLIFIER CIRCUITS WITH FULL SIGNAL MODULATION COMPENSATION TECHNIQUES WHICH ARE TECHNOLOGY INDEPENDENT	Issue Dt: 08/30/1994	Application #: 08080269	Filing Dt: 06/21/1993
25	Patent #: <u>5428305</u> Title: DIFFERENTIAL LOGIC LEVEL TRANSLATOR CIRCUIT WITH DUAL OUTPUT LOGIC LEVELS SELECTABLE BY POWER CONNECTOR OPTIONS	Issue Dt: 06/27/1995	Application #: 08129939	Filing Dt: 09/30/1993
26	Patent #: <u>5410274</u> Title: SINGLE-ENDED AND DIFFERENTIAL AMPLIFIERS WITH HIGH FEEDBACK INPUT IMPEDANCE AND LOW DISTORTION	Issue Dt: 04/25/1995	Application #: 08210269	Filing Dt: 03/17/1994
27	Patent #: <u>5592181</u> Title: VEHICLE POSITION TRACKING TECHNIQUE	Issue Dt: 01/07/1997	Application #: 08443519	Filing Dt: 05/18/1995
28	Patent #: <u>5572220</u> Title: TECHNIQUE TO DETECT ANGLE OF ARRIVAL WITH LOW AMBIGUITY	Issue Dt: 11/05/1996	Application #: 08443537	Filing Dt: 05/18/1995
29	Patent #: <u>5581213</u> Title: VARIABLE GAIN AMPLIFIER CIRCUIT	Issue Dt: 12/03/1996	Application #: 08479284	Filing Dt: 06/07/1995
30	Patent #: <u>5848160</u> Title: DIGITAL SYNTHESIZED WIDEBAND NOISE-LIKE WAVEFORM	Issue Dt: 12/08/1998	Application #: 08603673	Filing Dt: 02/20/1996

31	Patent #: <u>5729576</u> Title: INTERFERENCE CANCELING RECEIVER	Issue Dt: 03/17/1998	Application #: 08641452	Filing Dt: 04/30/1996
32	Patent #: <u>5684435</u> Title: ANALOG WAVEFORM COMMUNICATIONS REDUCED INSTRUCTION SET PROCESSOR	Issue Dt: 11/04/1997	Application #: 08653930	Filing Dt: 05/22/1996
33	Patent #: <u>5856760</u> Title: OVERDRIVE PROTECTION CLAMP SCHEME FOR FEEDBACK AMPLIFIERS	Issue Dt: 01/05/1999	Application #: 08745070	Filing Dt: 11/07/1996
34	Patent #: <u>5774318</u> Title: I.C. POWER SUPPLY TERMINAL PROTECTION CLAMP	Issue Dt: 06/30/1998	Application #: 08753647	Filing Dt: 11/27/1996
35	Patent #: <u>5960040</u> Title: COMMUNICATION SIGNAL PROCESSORS AND METHODS	Issue Dt: 09/28/1999	Application #: 08761103	Filing Dt: 12/05/1996
36	Patent #: <u>5870402</u> Title: MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH TIME DIVISION MULTIPLEXING	Issue Dt: 02/09/1999	Application #: 08764808	Filing Dt: 12/12/1996
37	Patent #: <u>5859568</u> Title: TEMPERATURE COMPENSATED AMPLIFIER	Issue Dt: 01/12/1999	Application #: 08827854	Filing Dt: 04/11/1997
38	Patent #: <u>5859558</u> Title: LOW VOLTAGE ANALOG FRONT END	Issue Dt: 01/12/1999	Application #: 08827855	Filing Dt: 04/11/1997
39	Patent #: <u>5859569</u> Title: CURRENT FEEDBACK DIFFERENTIAL AMPLIFIER CLAMP	Issue Dt: 01/12/1999	Application #: 08843200	Filing Dt: 04/14/1997
40	Patent #: <u>6040731</u> Title: DIFFERENTIAL PAIR GAIN CONTROL STAGE	Issue Dt: 03/21/2000	Application #: 08848930	Filing Dt: 05/01/1997
41	Patent #: <u>5859559</u> Title: MIXER STRUCTURES WITH ENHANCED CONVERSION GAIN REDUCED SPURIOUS SIGNALS	Issue Dt: 01/12/1999	Application #: 08903657	Filing Dt: 07/31/1997
42	Patent #: <u>6118811</u> Title: SELF-CALIBRATING, SELF-CORRECTING TRANSCEIVERS AND METHODS	Issue Dt: 09/12/2000	Application #: 08903807	Filing Dt: 07/31/1997
43	Patent #: <u>5990815</u> Title: MONOLITHIC CIRCUIT AND METHOD FOR ADDING A RANDOMIZED DITHER SIGNAL TO THE FINE QUANTIZER ELEMENT OF A SUBRANGING ANALOG-TO DIGITAL CONVERTER (ADC)	Issue Dt: 11/23/1999	Application #: 08941457	Filing Dt: 09/30/1997
44	Patent #: <u>5995535</u> Title: RAPID TIME AND FREQUENCY ACQUISITION OF SPREAD SPECTRUM WAVEFORMS VIA AMBIGUITY TRANSFORM	Issue Dt: 11/30/1999	Application #: 08965251	Filing Dt: 11/06/1997
45	Patent #: <u>5926123</u> Title: SELF CALIBRATION CIRCUITRY AND ALGORITHM FOR MULTIPASS ANALOG TO DIGITAL CONVERTER INTERSTAGE GAIN CORRECTION	Issue Dt: 07/20/1999	Application #: 08986942	Filing Dt: 12/08/1997
46	Patent #: <u>5973631</u> Title: TEST CIRCUIT AND METHOD OF TRIMMING A UNARY DIGITAL-TO- ANALOG CONVERTER (DZC) IN A SUBRANGING ANALOG-TO-DIGITAL CONVERTER (ADC)	Issue Dt: 10/26/1999	Application #: 09009612	Filing Dt: 01/20/1998
47	Patent #: <u>5963094</u> Title: MONOLITHIC CLASS AB SHUNT-SHUNT FEEDBACK CMOS LOW NOISE AMPLIFIER HAVING SELF BIAS	Issue Dt: 10/05/1999	Application #: 09027241	Filing Dt: 02/20/1998
48	Patent #: <u>6157224</u> Title: HIGH SPEED PIN DRIVER INTEGRATED CIRCUIT ARCHITECTURE FOR COMMERCIAL AUTOMATIC TEST EQUIPMENT APPLICATIONS	Issue Dt: 12/05/2000	Application #: 09219759	Filing Dt: 12/23/1998

	Patent #:	<u>6636730</u>	Issue Dt:	10/21/2003	Application #:	09220288	Filing Dt:	12/23/1998
49	Publication #:	<u>US20020055347</u>	Pub Dt:	05/09/2002				
	Title: WIDEBAND IF IMAGE REJECTING RECEIVER							
50	Patent #:	<u>6891424</u>	Issue Dt:	05/10/2005	Application #:	09408114	Filing Dt:	09/29/1999
	Title: MONOLITHIC PAYLOAD IF SWITCH							
51	Patent #:	<u>6552343</u>	Issue Dt:	04/22/2003	Application #:	09574123	Filing Dt:	05/18/2000
	Title: UNIT CELL WITH FAN-OUT FOR LARGE FOCAL PLANE ARRAYS WITH SMALL DETECTOR PITCH							
52	Patent #:	<u>6931083</u>	Issue Dt:	08/16/2005	Application #:	09579596	Filing Dt:	05/26/2000
	Title:	LOW NOISE, LOW DISTORTION, MUXABLE GILBERT MIXER SIGNAL PROCESSING SYSTEM AND METHOD WITH AGC FUNCTIONALITY						
53	Patent #:	<u>6535062</u>	Issue Dt:	03/18/2003	Application #:	09607223	Filing Dt:	06/30/2000
	Title: Low noise, low distortion, complementary IF amplifier							
54	Patent #:	<u>6693980</u>	Issue Dt:	02/17/2004	Application #:	09664298	Filing Dt:	09/18/2000
	Title: WIDEBAND FAST-HOPPING RECEIVER FRONT-END AND MIXING METHOD							
55	Patent #:	<u>6580383</u>	Issue Dt:	06/17/2003	Application #:	09703646	Filing Dt:	11/01/2000
	Title: HIGH RESOLUTION ADC BASED ON AN OVERSAMPLED SUBRANGING ADC							
56	Patent #:	<u>6975189</u>	Issue Dt:	12/13/2005	Application #:	09705134	Filing Dt:	11/02/2000
	Title: ON-CHIP MULTILAYER METAL SHIELDED TRANSMISSION LINE							
57	Patent #:	<u>6400229</u>	Issue Dt:	06/04/2002	Application #:	09790796	Filing Dt:	02/22/2001
	Title: LOW NOISE, LOW DISTORTION RF AMPLIFIER TOPOLOGY							
58	Patent #:	<u>6717450</u>	Issue Dt:	04/06/2004	Application #:	10144175	Filing Dt:	05/13/2002
	Title: MONOLITHIC I-LOAD ARCHITECTURE FOR AUTOMATIC TEST EQUIPMENT							
59	Patent #:	<u>6683904</u>	Issue Dt:	01/27/2004	Application #:	10144329	Filing Dt:	05/13/2002
	Publication #:	<u>US20030210737</u>	Pub Dt:	11/13/2003				
	Title: RF TRANSCEIVER WITH LOW POWER CHIRP ACQUISITION MODE							
60	Patent #:	<u>6882294</u>	Issue Dt:	04/19/2005	Application #:	10635826	Filing Dt:	08/06/2003
	Publication #:	<u>US20050030216</u>	Pub Dt:	02/10/2005				
	Title:	RESISTIVE LADDER, SUMMING NODE CIRCUIT, AND TRIMMING METHOD FOR A SUBRANGING ANALOG TO DIGITAL CONVERTER						
61	Patent #:	<u>6825697</u>	Issue Dt:	11/30/2004	Application #:	10689496	Filing Dt:	10/20/2003
	Title: HIGH-PERFORMANCE TRACK AND HOLD CIRCUIT							
62	Patent #:	NONE	Issue Dt:		Application #:	10698257	Filing Dt:	10/30/2003
	Publication #:	<u>US20040257125</u>	Pub Dt:	12/23/2004				
	Title: DIGITAL-TO-ANALOG CONVERTER WITH ALWAYS-ON CASCODE TRANSISTORS							
63	Patent #:	<u>7095347</u>	Issue Dt:	08/22/2006	Application #:	10715262	Filing Dt:	11/17/2003
	Publication #:	<u>US20040257058</u>	Pub Dt:	12/23/2004				
	Title: DIGITALLY TRIMMED DAC CELL							
64	Patent #:	<u>6879276</u>	Issue Dt:	04/12/2005	Application #:	10739860	Filing Dt:	12/18/2003
	Publication #:	<u>US20050035892</u>	Pub Dt:	02/17/2005				
	Title: SPLIT CELL BOWTIE DIGITAL TO ANALOG CONVERTER AND METHOD							

65	Patent #: <u>7071781</u> Publication #: <u>US20050035821</u> Title: HIGH SPEED, HIGH RESOLUTION AMPLIFIER TOPOLOGY	Issue Dt: 07/04/2006 Pub Dt: 02/17/2005	Application #: 10740172	Filing Dt: 12/18/2003
66	Patent #: <u>7098684</u> Publication #: <u>US20050035790</u> Title: HIGH SPEED SWITCH	Issue Dt: 08/29/2006 Pub Dt: 02/17/2005	Application #: 10740173	Filing Dt: 12/18/2003
67	Patent #: NONE Publication #: <u>US20050035788</u> Title: Clamped comparator	Issue Dt: Pub Dt: 02/17/2005	Application #: 10740334	Filing Dt: 12/18/2003
68	Patent #: NONE Publication #: <u>US20050038846</u> Title: Substraction circuit with a dummy digital to analog converter	Issue Dt: Pub Dt: 02/17/2005	Application #: 10847433	Filing Dt: 05/17/2004
69	Patent #: <u>7088148</u> Publication #: <u>US20050035791</u> Title: SAMPLE AND HOLD CIRCUIT AND BOOTSTRAPPING CIRCUITS THEREFOR	Issue Dt: 08/08/2006 Pub Dt: 02/17/2005	Application #: 10863561	Filing Dt: 06/08/2004
70	Patent #: <u>7098700</u> Publication #: <u>US20050127955</u> Title: LOW POWER OUTPUT DRIVER	Issue Dt: 08/29/2006 Pub Dt: 06/16/2005	Application #: 10886850	Filing Dt: 07/08/2004
71	Patent #: <u>7154421</u> Publication #: <u>US20050128118</u> Title: DNL/INL TRIM TECHNIQUES FOR COMPARATOR BASED ANALOG TO DIGITAL CONVERTERS	Issue Dt: 12/26/2006 Pub Dt: 06/16/2005	Application #: 10890443	Filing Dt: 07/12/2004
72	Patent #: NONE Publication #: <u>US20050083223</u> Title: Resolution enhanced folding amplifier	Issue Dt: Pub Dt: 04/21/2005	Application #: 10967963	Filing Dt: 10/19/2004
73	Patent #: <u>7253689</u> Publication #: <u>US20050270107</u> Title: LOW DISTORTION AMPLIFIER	Issue Dt: 08/07/2007 Pub Dt: 12/08/2005	Application #: 11148683	Filing Dt: 06/08/2005
74	Patent #: NONE Publication #: <u>US20060078065</u> Title: DIGITAL PRE-DISTORTION TECHNIQUE USING NONLINEAR FILTERS	Issue Dt: Pub Dt: 04/13/2006	Application #: 11150445	Filing Dt: 06/09/2005
75	Patent #: NONE Publication #: <u>US20060120479</u> Title: System and method for crest factor reduction	Issue Dt: Pub Dt: 06/08/2006	Application #: 11246027	Filing Dt: 10/07/2005
76	Patent #: NONE Publication #: <u>US20080247487</u> Title: Dynamic crest factor reduction system	Issue Dt: Pub Dt: 10/09/2008	Application #: 11784433	Filing Dt: 04/05/2007
77	Patent #: NONE Publication #: <u>US20080260066</u> Title: Dynamic digital pre-distortion system	Issue Dt: Pub Dt: 10/23/2008	Application #: 11788451	Filing Dt: 04/20/2007

Patent #: NONE Issue Dt: Application #: 11951238 Filing Dt: 12/05/2007
78 Publication #: US20080095265 Pub Dt: 04/24/2008

Title: DIGITAL PRE-DISTORTION TECHNIQUE USING NON-LINEAR FILTERS

Patent #: NONE Issue Dt: Application #: 12031249 Filing Dt: 02/14/2008
79 Publication #: US20080211583 Pub Dt: 09/04/2008

Title: SYSTEM AND METHOD FOR DYNAMIC DRAIN VOLTAGE ADJUSTMENT TO CONTROL LINEARITY, OUTPUT POWER, AND EFFICIENCY IN RF POWER AMPLIFIERS

Assignor

1 TELASIC COMMUNICATIONS, INC.

Exec Dt: 06/01/2009

Assignee

MICROELECTRONICS TECHNOLOGY, INC.

1 NO. 1 INNOVATION ROAD II
HSINCHU SCIENCE PARK, HSINCHU 300, TAIWAN, R.O.C.
HSINCHU, TAIWAN

Correspondence name and address

CARLOS R. VILLAMAR
7918 JONES BRANCH DRIVE, SUITE 500
MCLEAN, VA 22102

Search Results as of: 07/13/2009 10:52 AM
If you have any comments or questions concerning the data displayed, contact PRD / Assignments at 571-272-3350.
Web Interface last modified: October 18, 2008 v.2.0.2

| [HOME](#) | [INDEX](#) | [SEARCH](#) | [BUSINESS](#) | [CONTACT US](#) | [PRIVACY STATEMENT](#)